

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Application of :
Miwa WAKE et al. :
Serial No. (not yet assigned) :
Filed: January 28, 2004 :
For: METHOD OF MANUFACTURING :
A SEMICONDUCTOR :
INTEGRATED CIRCUIT AND :
SEMICONDUCTOR INTEGRATED :
CIRCUIT : Docket No. S004-4839 (DIV)

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MS PATENT APPLICATION
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

SECOND PRELIMINARY AMENDMENT

S I R:

Applicants preliminarily amend their application as
follows:

BY EXPRESS MAIL ON JANUARY 28, 2004 (ER 092 778 261 US)